

6. The circuitry of claim 2, wherein said phone line side circuitry further comprises phone line side DSL circuitry that may be used for receiving said DSL information transmitted across said isolation barrier.

7. The circuitry of claim 6, wherein said isolation barrier is comprised of a transformer.

8. The circuitry of claim 7, wherein said transformer powers said phone line side DSL circuitry.

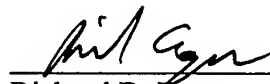
9. The circuitry of claim 7, wherein said DSL information is transmitted across said transformer.

CONCLUSION

Should any fees under 37 CFR 1.16-1.21 be required for any reason relating to the enclosed materials, the Commissioner is authorized to deduct such fees from Deposit Account No. 10-1205/SILA:057C1.

The examiner is invited to contact the undersigned at the phone number indicated below with any questions or comments, or to otherwise facilitate expeditious and compact prosecution of the application.

Respectfully submitted,



Richard D. Egan
Registration No. 36,788
Attorney for Applicant

O'KEEFE, EGAN & PETERMAN, LLP
1101 Capital of Texas Highway South
Building C, Suite 200
Austin, Texas 78746
(512) 347-1611
FAX: (512) 347-1615

APPENDIX
MARKED UP VERSION OF AMENDMENTS
AS REQUIRED BY RULE 121

In the Specification:

On page 1, please replace the paragraph from lines 7-19 with the following:

This application is a continuation of co-pending U.S. Serial No. 09/470,973 filed on December 22, 1999 which is [This is] a continuation-in-part of U.S. Serial No. 09/035,175 filed on March 4, 1998 which is a continuation-in-part of U. S. Serial Nos. 08/841,409, 08/837,702 and 08/837,714 all filed on April 22, 1997. Further, the following U. S. patent applications filed March 4, 1998, Serial No. 09/034,687, entitled "Digital Isolation System With Data Scrambling" by George Tyson Tuttle et al; Serial No. 09/034,456, entitled "Digital Isolation With ADC Offset Calibration; by Andrew W. Krone et al.; Serial No.09/034,455, entitled "Ring-Detect Interface Circuitry and Method for a Communication System" by Timothy J. Dupuis et al.; Serial No. 09/035,779, entitled "Call Progress Monitor Circuitry and Method for a Communication System" by Timothy J. Dupuis et al.; Serial No. 09/034,683, entitled "External Resistor and Method to Minimize Power Dissipation in DC Holding Circuitry for a Communication System" by Jeffrey W. Scott et al.; Serial No. 09/034,620, entitled "Caller ID Circuit Powered Through Hookswitch Devices" by Jeffrey W. Scott et al.; and Serial No. 09/034,682, entitled "Framed Delta Sigma Data With Unlikely Delta Sigma Data Patterns" by Andrew W. Krone et al., are expressly incorporated herein by reference.

In The Abstract:

An isolation system for terminating a phone line is provided. [that is suitable for use in telephony, medical instrumentation, industrial process control and other applications. Preferred embodiments of the] The invention may comprise a capacitive isolation barrier across which a digital signal is communicated. [The system provides a means of communication across the isolation barrier that is highly immune to amplitude and phase noise interference.] Clock recovery circuitry may be employed on one side of

the isolation barrier to extract timing information from the digital signal communicated across the barrier, [and to filter the effects of phase noise introduced at the barrier. Delta-sigma converters may be disposed on both sides of the isolation barrier to convert signals between analog and digital domains.] An isolated power supply may also be provided on the isolated side of the barrier, whereby direct current is generated in response to the digital data received across the isolation barrier. A bidirectional isolation system is provided whereby bidirectional communication of digital signals is accomplished using a single pair of isolation capacitors. In preferred embodiments, the digital data communicated across the barrier consists of digital delta-sigma data signals multiplexed in time with other digital control, signaling and framing information. Finally, the isolation system may include a pulse transformer to accommodate ADSL circuitry, whereby power is transmitted through the pulse transformer.

In The Claims:

10. (Canceled)
11. (Canceled)
12. (Canceled)
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